

Product Change Notice

Issue Date: 07 DEC 2012

Change Type:

1. Wafer fabrication location
2. IC change
3. Datasheet electrical specifications

Parts Affected:

HCPL-0723
HCPL-7723

All associated options and specials will also be affected. See Appendix for full part number list.

Description and Extent of Change:

1. Wafer fabrication transfers from site location United States to Taiwan.
2. Change in IC design.
3. Adjusted electrical specifications are input supply currents I_{DD1L} , I_{DD1H} , and output supply currents I_{DD2L} , I_{DD2H} .

New specifications:

Parameter	Symbol	Typ	Max	Units	Test Conditions
Logic Low Input Supply Current	I_{DD1L}	8.4	10.0	mA	$V_I = 0\text{ V}$
Logic High Input Supply Current	I_{DD1H}	0.6	3.0	mA	$V_I = V_{DD1}$
Logic Low Output Supply Current	I_{DD2L}	2.1	5.0	mA	$V_I = 0\text{ V}$
Logic High Output Supply Current	I_{DD2H}	2.0	5.0	mA	$V_I = V_{DD1}$

Current specifications:

Parameter	Symbol	Typ	Max	Units	Test Conditions
Logic Low Input Supply Current	I_{DD1L}	7	10	mA	$V_I = 0\text{ V}$
Logic High Input Supply Current	I_{DD1H}	1.8	3	mA	$V_I = V_{DD1}$
Output Supply Current	I_{DD2L}	12.5	17.5	mA	
	I_{DD2H}	12	16.5	mA	

Reasons for Change:

1. Wafer fabrication process and facilities obsolescence by IC wafer subcontractor
2. Change in IC design is to improve the robustness of products
3. The datasheet electrical specifications are adjusted to reflect the product and process true performance.

Effect of Change on Fit, Form, Function, Quality, or Reliability:

All other remaining electrical specifications in datasheet and physical characteristics remain the same. Appropriate electrical characterization and reliability qualification have been performed on representative

products to insure normal parametric distribution, consistent electrical performance, and reliability. Final changes will be reflected in the datasheets after completion of electrical characterization.

Effective Date of Change:

Implementation of the change will be effective from product date code 1310.

Timing of shipment of the changed part will vary by part number depending on qualification completion, customer demand, and inventory levels.

Datasheet changes take effect from 07 March 2013. Datasheet publication is to be released on Avago's website from 07 March 2013.

Qualification Data:

Qualification data has been generated and approved.

These changes have been reviewed and approved by Avago Technologies engineers and managers per Avago Technologies procedure: Change Control and Customer Notification, A-5962-6052-80.

Please contact your Avago Technologies field sales engineer or Contact Center (<http://www.avagotech.com/contact/>) for any questions or support requirements. Please return any response as soon as possible, but not to exceed 30 days.

APPENDIX

Affected Part Number	Part Description
HCPL-0723	Optocoupler(50Mbd,2ns PWD),SO8
HCPL-0723-000E	Optocoupler(50Mbd,2ns PWD),SO8+LF
HCPL-0723-060	Optocoupler(50Mbd,2ns PWD),VDE
HCPL-0723-060E	Optocoupler(50Mbd,2ns PWD),IEC+LF
HCPL-0723-500	Optocoupler(50Mbd,2ns PWD),T/R
HCPL-0723-500E	Optocoupler(50Mbd,2ns PWD),T/R+LF
HCPL-0723-560	Optocoupler(50Mbd,2ns PWD),T/R+VDE
HCPL-0723-560E	Optocoupler(50Mbd), T/R+VDE+L/F
HCPL-7723	Optocoupler(50Mbd,2ns PWD),PDIP8
HCPL-7723-000E	Optocoupler(50Mbd,2ns PWD), L/F
HCPL-7723-020	Optocoupler(50Mbd,2nsPWD),DIP+5KV
HCPL-7723-020E	Coupler(50Mbd,2nsPWD)DIP 5KV+LF
HCPL-7723-060	Optocoupler(50Mbd,2ns PWD),VDE
HCPL-7723-060E	Optocoupler(50Mbd,2ns PWD),IEC+LF
HCPL-7723-300	Optocoupler(50Mbd,2ns PWD),SMD
HCPL-7723-300E	Optocoupler(50Mbd),SMD,LF
HCPL-7723-320	Optocoupler(50Mbd,2nsPWD),SMD+5KV
HCPL-7723-320E	Optocoupler(50Mbd),SMD+UL, LF
HCPL-7723-360	Optocoupler(50Mbd,2ns PWD),SMD+VDE
HCPL-7723-360E	Coupler(50Mbd,2ns PWD),SMD+VDE+LF
HCPL-7723-500	Optocoupler(50Mbd,2ns PWD),T/R
HCPL-7723-500E	Optocoupler(50Mbd,2ns PWD),T/R+LF
HCPL-7723-50FE	Optocoupler(50Mbd,2ns PWD),T/R+LF
HCPL-7723-520	Optocoupler(50Mbd,2nsPWD),T/R+5KV
HCPL-7723-520E	Coupler(50Mbd,2nsPWD)T/R+5KV+LF
HCPL-7723-560	Optocoupler(50Mbd,2ns PWD),T/R+VDE
HCPL-7723-560E	Coupler(50Mbd,2ns PWD),T/R+VDE+LF