CHANGE NOTIFICATION

Linear Technology Corporation
1630 McCarthy Blvd., Milpitas, CA 95035-7417
(408) 432-1900

March 4, 2011

PCN#: 030411

Dear Sir/Madam:

Subject: Notification of Change to LT4351 Datasheet

Please be advised that Linear Technology Corporation has made a minor change to the LT4351 product datasheet to better center the parametric distribution within the specification range. The change is shown on the attached page of the marked up datasheet. There was no change made to the die. The product shipped after April 4, 2011 will be tested to the new limits.

Should you have any further questions, please feel free to contact me at 408-432-1900 ext. 2519, or by e-mail at NGirn@Linear.com. If I do not hear from you by April 4, 2011, we will consider this change to be approved by your company.

Sincerely,

Naib Girn
Quality Assurance Manager

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{IN} = V_{OUT} = 5V, V_{DD} = 16.1V, V_{UV} = 0.4V, V_{OV} = 0.2V, GATE Open, unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
I_{OV}	OV Input Bias Current	V_{OV} = $V_{OV(TH)}$ – 10mV	●		–100	–400	V
$V_{F(ON)}$	$\overline{FAULT}$ Pin On-Voltage	I_F = 5mA in Fault Condition	●		0.14	0.25	V
$I_{F(OFF)}$	$\overline{FAULT}$ Pin Leakage Current	V_F = 30V, V_{IN} = 4.9V	●		0.04	1	µA
Boost Supply							
V_{BR}	Boost Regulation Trip Voltage	Measured as V_{DD} to V_{IN}, Rising Edge	●	10.2	10.7	~~11.1~~ 11.4	V
t_{OFF}	Boost Supply Off-Time				600		ns
I_{SWLIM}	Boost Supply Switch Current Limit		●	350	450	650	mA
Gate Drive							
V_{IOR}	Input-to-Output Regulated Voltage		●	4	15	25	mV
ΔV_{GL}	Gate Voltage Limit	V_{IN} = 5V, V_{OUT} = 4.9V, V_{DD} = 13V Measured with Respect to V_{DD}	●		–2.3	–3	V
$\Delta V_{G(MAX)}$	Maximum Gate Voltage	V_{IN} = 5V, V_{OUT} = 4.9V, V_{DD} = 16.1V Measured with Respect to V_{OUT}	●	7	7.4	7.8	V
$V_{G(OFF)}$	Gate Off-Voltage	V_{OUT} = 5.1V	●		0.16	0.30	V
I_{GSO}	Gate Source Current	V_{OUT} = 4.9V, V_{GATE} = 9V			0.670		A
I_{GSK}	Gate Sink Current	V_{OUT} = 5.1V, V_{GATE} = 9V			0.670		A
V_{DD}	Operating Range		●			30	V
I_{VDD}	V_{DD} Supply Current	V_{IN} = 1.2V, V_{OUT} = 1.1V, V_{DD} = 12.3V, GATE Open V_{IN} = 18V, V_{OUT} = 17.9V, V_{DD} = 29.1V, GATE Open	● ●		3 3.6	4 5.6	mA mA
Status Functions							
ΔV_{GIS}	Minimum Gate Voltage for Turning On Status	V_{OUT} = 4.9V, I_{STATUS} = 1mA	●		0.75	1	V
V_{IOGF}	V_{IN} to V_{OUT} Fault Voltage with Open Gate	V_{OUT} Falling, Measured with Respect to V_{IN}		185	210	230	mV
$V_{ST(ON)}$	Status Pin On-Voltage	I_{ST} = 5mA, V_{OUT} = 4.9V, Status On	●		0.13	0.25	V
$I_{ST(OFF)}$	Status Pin Leakage Current	V_{ST} = 30V, Status Off, V_{IN} = 4.9V	●		0.04	1	µA

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula:

$T_J = T_A + (P_D \bullet 120°C/W)$