

CHANGE NOTIFICATION



Linear Technology Corporation
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March 30, 2012

PCN#: 033012

Dear Sir/Madam:

Subject: Notification of Change to LTC2924 Die

Please be advised that Linear Technology Corporation has made a minor change to the LTC2924 die in order to improve the device performance. A bug in the internal logic caused the TMR pin to exhibit erratic behavior. This behavior does not interfere with the operation of the part within an application. The logic bug limits the ratio of the capacitor values on the PGT and TMR pins to less than 40 to avoid unintended multiple TMR pin pulsing. The fix allows the customer to use a PGT: TMR capacitance ratio of up to 100.

No other functional or parametric specifications are affected and the datasheet is unchanged. The change was qualified by performing extensive characterization over the full operating temperature range and Op-Life test on 77 units at 125C for 500 hrs. Product built using the new die will be shipped with a datecode of approximately 1218.

Should you have any further questions, please feel free to contact me at 408-432-1900 ext. 2519, or by e-mail at NGIRN@linear.com. If I do not hear from you by April 30, 2012, we will consider this change to be approved by your company.

Sincerely,

Naib Girn
Quality Assurance Manager

Confidential Statement

This change notice is for Linear Technology's Customers only.
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