



Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

PRODUCT INFORMATION NOTIFICATION

PIN: PIN145353

Date: December 19, 2014

Subject: Qualification of OSE as an additional Test and Finish Location for Cypress Products

To: HIRONO KAKINUMA
CYPRESS INTERNAL
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Change Type: Major

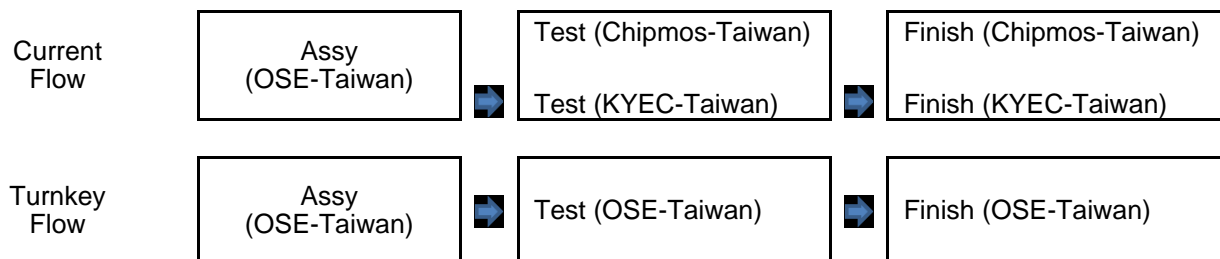
Description of Change:

Cypress announces the planned qualification of package-level Test and Finish manufacturing operations at Orient Semiconductor Electronics, Ltd (OSE) in Kaohsiung, Taiwan for Cypress products that are currently being assembled at OSE and tested at King Yuan Electronics Co., Ltd., (KYECC) Taiwan and ChipMOS Technologies Inc., Hsinchu, Taiwan. There will be no changes to the tester platforms, test program coverage or test methodologies.

OSE is one of the world's leading electronics contract manufacturing services providers. Their assembly facility in Kaohsiung, Taiwan is already a qualified site for Cypress products. This qualification will enable these products to be assembled, tested, finished and shipped directly to Cypress customers from one subcontractor facility. This qualification will allow Cypress to leverage OSE's manufacturing expertise and quality focus. This in turn also provides the means for Cypress to continue to meet its customers' IC testing needs as well as delivery commitments in dynamic, changing market conditions. OSE is certified on several international quality standards: ISO / TS16949, ISO 9001 and ISO 14001.

Benefit of Change:

This change will provide a turnkey solution for these products that are being manufactured in Cypress' subcontractors. This will provide faster delivery of products to Cypress' customers.



Affected Part Numbers: 355

Affected Parts: Please see the attached file for the list of parts affected by this notice.

Qualification Status:

The qualification report for the OSC50 test platform can be found as an attachment to this notification or by visiting www.cypress.com and typing the QTP number in the keyword search window. The qualification reports for other package types and tester platforms will be available in January 2015 to March 2015.

Please refer to table below for the complete qualification schedules. Note that the schedules are estimated dates based on current projections, and are subject to change.

Tester Platform	QTP Number	Expected Date of Completion
OSC50	144802	December 2014
ADVANTEST	144806	March 2015
NEXTEST	144902 144901	March 2015
FINISH – TSSOP Package	144701	February 2015
FINISH – SSOP Package	144905	March 2015
FINISH – TSOP Package	144906	March 2015

Approximate PIN Implementation Date:

Implementation is expected to start in December 2014 and will be completed by March 2015.

Anticipated Impact:

Products that will be tested at OSE Taiwan will be completely compatible with existing products from a functional, parametric, and quality performance perspective.

Cypress also recommends that customers take this opportunity to review these changes against their current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

Response Required:

This is an information announcement only. No response is required

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration