

Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

# PRODUCT INFORMATION NOTIFICATION

**Subject:** Qualification of Cypress Bangkok as an Additional Test and Finish Site for Select 48-Lead TSOP I Pb-Free Packages for Asynchronous SRAM Products

To: TOKYO ELECTRON DEVICE

VÒÖ

cy#inside@teldevice.co.jp

**Change Type:** Minor

## **Description of Change:**

Cypress announces the qualification of package-level test and finish operations at Cypress Bangkok (229 Moo 4 Changwattana Road Pakkred, Nonthaburi 11120 Thailand) for select 48-Lead TSOP I Pb-free packages for Asynchronous SRAM products. The qualification of this facility will enable Cypress to have turnkey process solutions to meet our customers' stringent quality and reliability requirements in our effort to continually provide world-class services.

There are no changes to ordering part numbers. Product datasheets remain the same and can be downloaded from the Cypress Website (<a href="www.cypress.com">www.cypress.com</a>).

### **Benefit of Change:**

This qualification is part of Cypress' continuous improvement in our flexible manufacturing initiative providing Cypress with the added capability to meet upside market demand, reduce business continuity risk, and ensure consistent and reliable delivery to customers in dynamic, changing market conditions.

## Part Numbers Affected: 52

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change. Note that any new parts that are introduced after the publication of this PIN will include all changes outlined in this PIN.

#### **Qualification Status:**

This change has been qualified through a series of tests documented in the Qualification Test Plans QTP#180420 and QTP#180906. These qualification reports can be found as attachments to this PIN.

# **Approximate Implementation Date:**

This change will be effective with the date of this notification.

### **Anticipated Impact:**

Products manufactured at Cypress Bangkok are completely compatible with existing product from a functional, parametric, and quality performance perspective.

Cypress also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

### Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

# **Response Required:**

This is an information only announcement. No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn\_adm@cypress.com.

Sincerely,

Cypress PCN Administration