



Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

PRODUCT INFORMATION NOTIFICATION

PIN: PIN194301

Date: October 21, 2019

Subject: V_{DD} Power-Down Ramp Rate (t_{VF}) Specification Change in the Datasheet for Select F-RAM Devices

To: TOKYO ELECTRON DEVICE
TELDEVICE
cy-inside@teldevice.co.jp

Change Type: Minor

Description of Change:

Cypress announces a V_{DD} power-down ramp rate (t_{VF}) specification change for 16Kb, 4.5-5.5 V, I2C F-RAM. The t_{VF} specification (guaranteed-by-design, not a production tested spec) will be changed from 3.3 $\mu\text{s}/\text{V}$ to 30 $\mu\text{s}/\text{V}$ in the datasheet. The purpose of this change is to standardize the t_{VF} spec across the serial F-RAM product family. There is no change to silicon design, test methodology or manufacturing sites, and hence we expect end applications to be unaffected.

The updated product datasheet is attached to this notification.

Part Numbers Affected: 2

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change.

Approximate Implementation Date:

This change will be effective with the date of this notification.

Anticipated Impact:

There is no change to the product. Products manufactured with revised datasheet parameter are completely compatible with existing product from fit, form, functional, parametric, and quality performance perspectives.

Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

Response Required:

This is an information only announcement. No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration