

Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

# **PRODUCT INFORMATION NOTIFICATION**

## **PIN:** PIN200901

**Date:** February 26, 2020

**Subject:** Qualification of Amkor Technology Japan Usuki Site for 300mm DPS Process for Select MCU Products

To: TOKYO ELECTRON DEVICE TELDEVICE cy-inside@teldevice.co.jp

## Change Type: Minor

## **Description of Change:**

Cypress announces the qualification of Amkor Technology Japan Usuki (ATJ, 1913-2, Takegashita, Fukura, Usuki-shi,Oita 875-0053, Japan) as a new Die Processing Service (DPS) site for select MCU Products. The current products are processed at Amkor Technology Japan Kitsuki (ATJ, 2820-2, Minami-Kitsuki, Kitsuki-shi, Oita 873-0002, Japan). The change corresponds to Category (DPS Usuki) in APCN194303.

Amkor Technology Japan Usuki is certified by international quality standards, ISO 9001 and IATF16949. Amkor Technology Japan certificates can be viewed on their corporate web site below: <u>https://amkor.com/quality-management/</u>

#### Benefit of Change:

Cypress will have the added capability to meet varying market demand, and to ensure consistent and reliable delivery performance to customers.

#### Part Numbers Affected: 109

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change. Note that any new parts that are introduced after the publication of this PIN will include all changes outlined in this PIN.

#### **Qualification Status:**

This DPS site has been qualified through a series of tests documented in Qualification Test Plan QTP#193212. This qualification report can be found as an attachment to this PCN.

QTP Number	Qualification
193212	DPS Line, 300mm Wafer, Amkor Technology Japan Usuki Site

# **Approximate Implementation Date:**

Effective 90 days from the date of this notification, all shipments of the affected part numbers in the attached file will be supplied from Amkor Technology Japan Usuki.

## Anticipated Impact:

No impact is expected in datasheet parameters, package composition and package pin-out.

## Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

## **Response Required:**

No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at <u>pcn\_adm@cypress.com</u>.

Sincerely,

Cypress PCN Administration