

Product and Process Change Notice

PCN No.	PCN03041	PCN Date	7/14/2021	Effective Date	10/12/2021
Title	SilTerra as additional CMOS wafer processing site for 16Mb x8/16 and 8Mb x16 BGA products.				

Everspin is adding SilTerra as a 2 nd qualified CMOS wafer processing site for 16Mb x8/16 and 8Mb x16 BGA products. No change to MRAM wafer processing.				
Reason For Change				
In order to increase capacity and improve supply flexibility, Everspin is adding SilTerra as a CMOS wafer processing site for 16Mb x8/16 and 8Mb x16 BGA products.				
Affected Products				
Part Number	Description	Part Number	Description	
MR4A16BMA35	Commercial Temp.	MR4A16BMA35R	Commercial Temp., T&R	
MR4A16BCMA35	Industrial Temp.	MR4A16BCMA35R	Industrial Temp., T&R	
MR4A08BMA35	Commercial Temp.	MR4A08BMA35R	Commercial Temp., T&R	
MR4A08BCMA35	Industrial Temp.	MR4A08BCMA35R	Industrial Temp., T&R	
MR3A16AMA35	Commercial Temp.	MR3A16AMA35R	Commercial Temp., T&R	
MR3A16ACMA35	Industrial Temp.	MR3A16ACMA35R	Industrial Temp., T&R	
Impact on Form, Fit, Function, Quality or Reliability				
No impact				
Proposed First Ship Date for Change:				
October 12, 2021				
Key Material Differences				
	Site	Old	New	
	CMOS FAB	TSMC FAB11	TSMC FAB11 or SilTerra	
	MRAM FAB	Everspin Chandler	Everspin Chandler	
Product Identifier				
There will be no changes to part number. Everspin maintains traceability of product by trace code marked on the package.				
Supplier Qual Plan Schedule and Results				
SilTerra process qualification is complete and all Everspin requirements met; a qualification report is available on request.				
Date Qualification Samples Are Available:				
Samples are generally available now but please request your specific part number to your Everspin Sales contact.				
Acceptance of Change				
Everspin will consider this change accepted unless specific conditions for acceptance are provided in writing within 30 days of receipt of this notice.				

Product and Process Change Notice

PCN No.	PCN03041	PCN Date	7/14/2021	Effective Date	10/12/2021
Title	SilTerra as additional CMOS wafer processing site for 16Mb x8/16 and 8Mb x16 BGA products.				

Contact Information		Sample Information ☒ Samples Available Now	
Product Marketing Everspin Technologies (480) 347-1111		Contact Everspin sales: http://www.everspin.com/contact-us-everspin-technologies If using the on-line sample request, please refer to this PCN # to receive samples.	
Originator			
<u>Date</u> 7/6/2021	<u>Title</u> Senior Quality Engineer	<u>Name</u> Paul Drobny 	
Approval and Release			
<u>Date</u> 7/6/2021	<u>Title</u> Senior Director of Fab Ops & Quality	<u>Name</u> Khalidoun Barakat 	
<u>Date</u> 7/6/2021	<u>Title</u> Senior Director of Operations	<u>Name</u> Amit Shah 	
<u>Date</u> 7/6/2021	<u>Title</u> VP of Sales & Marketing	<u>Name</u> Troy Winslow 	