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## Chip Errata

### A-009204: System bus may hang if software register SYSCTL[RSTD] is set while DMA transactions are active

**Affects:** eSDHC

**Description:** In the event of that any data error (like, IRQSTAT[DCE]) occurs during an eSDHC data transaction where DMA is used for data transfer to/from the system memory, setting the SYSCTL[RSTD] register may cause a system hang. If software sets the register SYSCTL[RSTD] to 1 for error recovery while DMA transferring is not complete, eSDHC may hang the system bus. This happens because the software register SYSCTL[RSTD] resets the DMA engine without waiting for the completion of pending system transactions.

**Impact:** Causes system bus to hang.

**Workaround:** Add a 5 ms delay before setting SYSCTL[RSTD] to make sure all the DMA transfers are finished.