



Advance Product Change Notification

201705010A

Issue Date: 04-Jun-2017



Management Summary

NXP is sending an Advanced ePCN notice to i.MX 6 and i.MX 7 customers to communicate that a final PCN for a new silicon revision is expected.

Change Category

- | | | | | |
|--|---|--|---|---|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input checked="" type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |

i.MX Product Line Silicon Revisions

Details of this Planned Change

NXP is notifying i.MX 6 and i.MX 7 customers in advance that a new silicon revision is planned soon for the following product lines. The silicon revision will be to improve performance for the existing ROM. A Final PCN (FPCN) will be sent with details for each respective product line once completed. Details in the FPCN will again include sample date availability as well as main production availability. In addition, sample part numbers and orderable part numbers will be listed on the FPCN when it is distributed.

The following product lines will have silicon revisions with new orderable part numbers for customers. NXP will continue to use older revisions in parallel for customers who do not plan to change to the new part number.

These are the expected sample date availabilities for the new silicon revisions, subject to change:

i.MX 6Dual 26-Jul-17
i.MX 6Quad 26-Jul-17
i.MX 6DualPlus 15-Aug-17
i.MX 6QuadPlus 15-Aug-17
i.MX 6Solo 01-Sep-17
i.MX 6DualLite 01-Sep-17
i.MX SoloLite 01-Aug-17
i.MX SoloX 15-Sep-17
i.MX 7Dual 15-Jul-17
i.MX 7Solo 15-Jul-17
i.MX 6ULL 10-Oct-17

Why do we Plan this Change

The silicon revision will introduce improved performance for the existing ROM.

Product Availability

Sample Information

Sample planning follows with the final PCN

Impact

There is no change to product form, fit, function, or reliability.

Data Sheet Revision

A new datasheet will be issued

Disposition of Old Products

Existing inventory will be shipped until depleted

Additional information

Self qualification: [view online](#)

Timing and Logistics

The Self Qualification Report will be ready on 01-Oct-2017.

The Final PCN is planned to be issued on: 02-Oct-2017.

Your acknowledgement of this change, conform JEDEC JESD46 D, is expected till 04-Jul-2017.

F-PCN's will be sent for each respective product line to announce dates of product availability and new part numbers.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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