



## Final Product Change Notification

201804013F01

**Issue Date:** 24-May-2018

**Effective Date:** 22-Aug-2018



# QUALITY

### Management Summary

NXP Semiconductors announces the MC9S08AWXX 0N20A maskset update to 1N20A for OSC drive current increase and ICG fix.

### Change Category

- |  |   |  |   |   |
|--|---|--|---|---|
| <input type="checkbox"/> Wafer Fab Process   | <input type="checkbox"/> Assembly Process   | <input type="checkbox"/> Product Marking           | <input type="checkbox"/> Test Location  | <input checked="" type="checkbox"/> Design              |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification  | <input type="checkbox"/> Test Process   | <input checked="" type="checkbox"/> Errata              |
| <input type="checkbox"/> Wafer Fab Location  | <input type="checkbox"/> Assembly Location  | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware            | <input type="checkbox"/> Other              |  |   |   |

## MC9S08AWXX TSMC3 Maskset Update to 1N20A for Design Fix

### Description of Change

NXP Semiconductors is announcing a new version of silicon for the MC9S08AWXX 0N20A product line. The new silicon mask revision will be 1N20A.

An errata document is generated for the MC9S08AW60/48/32/16 Mask 1N20A devices associated with this notification. The document provides a detailed description of the changes, along with existing deviations from datasheet identified on current production devices. Potential implications on customer applications are described.

1N20A:

The following erratum was removed.

SE194-STOP3DCO: Stop3 Mode DCO Erratum.

The following erratum was added.

SE198B-ICG-RANGE=0, HGO=1: Oscillator Mode Erratum.

The errata MSE9S08AW60\_ 1N20A revision 0 can be found at:

[http://www.nxp.com/products/automotive-products/microcontrollers-and-processors/arm-mcus-and-mpus/s32-arm-processors-microcontrollers/8-bit-general-purpose-aw60-48-32-16-mcus:S08AW?tab=Documentation\\_Tab](http://www.nxp.com/products/automotive-products/microcontrollers-and-processors/arm-mcus-and-mpus/s32-arm-processors-microcontrollers/8-bit-general-purpose-aw60-48-32-16-mcus:S08AW?tab=Documentation_Tab)

### Reason for Change

The release of MC9S08AWXX 1N20A maskset fixes Erratum SE194-STOP3DCO: Stop3 Mode DCO.

### Identification of Affected Products

Top side marking

The maskset information in the marking will be changed from 0N20A to 1N20A.

## Product Availability

### Sample Information

Samples are available from 26-Apr-2018  
Part number is PC9S08AW60MFUE

### Production

Planned first shipment 24-Jul-2018

## Anticipated Impact on Form, Fit, Function, Reliability or Quality

There is no change to product form, fit, function, or reliability, for the new identified errata, there are potential hardware and / or software implications to customers.

### Data Sheet Revision

No impact to existing datasheet

### Disposition of Old Products

Existing inventory will be shipped until depleted

## Additional information

Self qualification: [view online](#)  
Additional documents: [view online](#)

## Timing and Logistics

In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by 23-Jun-2018.

## Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

<b>Name</b>	Yuanyuan Gu
<b>Position</b>	Product engineer
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At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.  
Customer Focus, Passion to Win.

NXP Quality Management Team.

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