



Customer Information Notification

2020010101

Issue Date: 31-Jan-2020
Effective Date: 01-Feb-2020



Management Summary

Release of new errata document for T1040/42 and T1024/23 Family of products

Change Category

- | | | | | |
|--|---|--|---|---|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input checked="" type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

Release of Errata Document for T1040/42 and T1024/23 Family of Products

Description

The following changes were made to the errata documents for T1024/14 and T1023/13 family of devices:

- 1- Added Ethernet A-011436 "The Ethernet MAC, mEMAC, may experience a TX hang OR transmit confirmations could become out of sync AND the 1588 feature may become out of sync if a link fault occurs during the transmission of the first two words of a frame"
- 2- Added PCIe A-050155 "Expansion ROM is only supported on PCI Express controller 1"
- 3- Updated DDR A-009942 "DDR controller can train to non-optimal setting"
- 4- Updated eSDHC A-008171 "eSDHC transactions may fail in tuning modes of operation when the input data window is close to 1 UI during tuning"
- 5- Removed CPC A-006001

The following changes were made to the errata documents for T1040/20 and T1042/22 family of devices:

- 1- Added PCIe A-050155 "Expansion ROM is only supported on PCI Express controller 1"
- 2- Updated Ethernet A-009885 "MDIO read transaction from external PHY may return corrupted data"
- 3- Updated DDR A-009942 "DDR controller can train to non-optimal setting"
- 4- Updated eSDHC A-008171 "eSDHC transactions may fail in tuning modes of operation when the input data window is close to 1 UI during tuning"

Reason

Notify customers with latest errata updates.

Identification of Affected Products

Refer to attached errata documents.

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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