

# Customer Information Notification

2020030011

Issue Date:22-May-2020Effective Date:23-May-2020



## **Management Summary**

NXP Semiconductors announces the Datasheet update for MKE02/MKE04/MKE06.

# **Change Category**

| Wafer Fab   | Assembly   | Product Marking                   | Test      | Design              |
|-------------|------------|-----------------------------------|-----------|---------------------|
| Process     | Process    | _                                 | Location  | -                   |
| Wafer Fab   | Assembly   | Mechanical                        | Test      | Errata              |
| Materials   | Materials  | Specification                     | Process   |                     |
| U Wafer Fab |            | De alvia a (Obia aix a (Labalia a | Test      | Electrical          |
| Location    | Location   | Packing/Shipping/Labeling         | Equipment | spec./Test coverage |
| Firmware    | Other - Da | tasheet                           |           |                     |

# MKE02/MKE04/MKE06 Datasheet Update

## Description

NXP Semiconductors announces that the MKE04Z8 Datasheet has been updated to Rev 5(MKE04P24M48SF0\_Rev.5).

NXP Semiconductors announces that the MKE02 Datasheet has been updated to Rev 6(MKE02P64M40SF0\_Rev.6) and Rev 7(MKE02P64M20SF0\_Rev.7). NXP Semiconductors announces that the MKE06,MKE04Z64/128 Datasheet has been updated to Rev 6 (MKE04P80M48SF0 Rev.6, MKE06P80M48SF0 Rev.6).

Changes are summarized below:

MKE04P24M48SF0\_Rev.5: Page5, Updated the descriptions of ILAT to add the missing temperature in the ESD handling ratings. Page7, Updated Maximum power supply VDD ramp-up rate from 500V/ms to 70V/ms in footnote2. Page16&17, Corrected the Unit to the tTCLK, tclkh, tclkl, and tICPW, and added footnote to the Units in the FTM module timing. Page26, Updated the timing assumptions in the SPI switching specifications

MKE02P64M40SF0\_Rev.6 & MKE02P64M20SF0\_Rev.7: Page8, Updated Maximum power supply VDD ramp-up rate from 500V/ms to 70V/ms in footnote2.

MKE04P80M48SF0\_Rev.6/MKE06P80M48SF0\_Rev.6: Page8, Updated Maximum power supply VDD ramp-up rate from 500V/ms to 70V/ms in footnote2.

Page28, Updated the timing assumptions in the SPI switching specifications.

The updated Datasheet can be found at:

https://www.nxp.com/search?category=documents&keyword=MKE&filter=type>>Data Sheet&siblings=false

#### Reason

The Datasheets have been updated to provide additional technical clarification on some device features.

#### Identification of Affected Products

Product identification does not change

#### Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

#### **Data Sheet Revision**

A new datasheet will be issued

#### Additional information

Additional documents: view online

#### **Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please <u>contact</u> <u>NXP "Global Quality Support Team"</u>.

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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|----------------|--|
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At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards. Customer Focus, Passion to Win.

NXP Quality Management Team.

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