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## PRODUCT BULLETIN

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ISSUE DATE: 30-Sep-2014

NOTIFICATION: 16462

TITLE: MPIC Errata for Power Architecture Devices: e500, e600, e5500, and e6500-  
Based Cores

EFFECTIVE DATE: 01-Oct-2014

### **DEVICE(S)**

MPN
B4420NSE7QQMD
B4420NXE7QQMD
B4860NSE7QUMD
B4860NSN7QUMD
B4860NXE7QUMD
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**AFFECTED CHANGE CATEGORIES**

- ERRATA

**DESCRIPTION OF CHANGE**

Errata A-008312: Duplicate edge-triggered interrupt after priority re-arbitration.

There is an occurrence of duplicate interrupt when an edge-triggered interrupt higher in priority comes closely to any other enabled interrupts. The following is the sequence of events that leads to the duplicate edge-triggered interrupt::

1. An active interrupt is waiting for acknowledgement
2. An edge-triggered interrupt of higher priority triggers closely to the lower priority interrupt just when it is acknowledged
3. The higher priority edge-triggered interrupt supersedes and fires a new interrupt to the core
4. The core acknowledges the higher priority interrupt without clearing the pending state and

finishes the interrupt service routine with EOI

5. A duplicate of the higher priority edge-triggered interrupt is triggered because of the uncleared pending state

Workaround:

Chose one of the following workarounds based on the interrupt type:

- Configure the higher priority interrupts as level-sensitive only
  - a. In case of IRQs this can be configured in the Vector/Priority Register.
  - b. It is not an option for global timers or IPI.
- Any enabled edge-triggered interrupts must be no higher in priority than the other enabled interrupts.

Resolution:

No plan to fix

## **REASON FOR CHANGE**

Enabling any edge-triggered interrupts higher in priority than other enabled interrupts may lead to the duplicate edge-triggered interrupt. This includes edge-triggered IRQs, global timers and IPI.

## **ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)**

No change to fit, form, function and reliability.

## **NOTE:**

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THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to [www.freescale.com](http://www.freescale.com)

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**QUAL DATA AVAILABILITY DATE:** 01-Jan-2015

**QUALIFICATION STATUS:** N/A

**QUALIFICATION PLAN:**

N/A

**RELIABILITY DATA SUMMARY:**

N/A

**ELECTRICAL CHARACTERISTIC SUMMARY:**

N/A

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**CHANGED PART IDENTIFICATION:**

N/A

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**ATTACHMENT(S):**

External attachment(s) for this notification can be viewed at:

[16462\\_A-008312\\_MPIC\\_Double\\_Interrupt.pdf](#)

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