



## PRODUCT BULLETIN

Generic Copy

This notice is Freescale Confidential Proprietary and is only intended for the customer listed on this notification.

ISSUE DATE: 17-Dec-2014  
 NOTIFICATION: 16581  
 TITLE: P1022/P1013 DEEP SLEEP HANG ERRATA  
 EFFECTIVE DATE: 18-Dec-2014

DEVICE(S)

MPN
P1013NSE2HFB
P1013NSE2LFB
P1013NSE2MHB
P1013NSN2EFB
P1013NSN2HFB
P1013NSN2LFB
P1013NXE2HFB
P1013NXE2LFB
P1013NXN2EFB
P1013NXN2HFB
P1013NXN2LFB
P1022NSE2HFB
P1022NSE2LFB
P1022NSE2LHB
P1022NSE2MHB
P1022NSN2EFB
P1022NSN2HFB
P1022NSN2LFB
P1022NSN2MHB
P1022NXE2HFB
P1022NXE2LFB
P1022NXN2HFB
P1022NXN2LFB

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AFFECTED CHANGE CATEGORIES

- ERRATA

DESCRIPTION OF CHANGE

Please refer to the Device erratum L2C A-007836 as attachment

Title:

The system may hang upon enabling L2 cache, after coming out of deep sleep

Description:

The system may hang when L2 is re-enabled after waking up from deep sleep if all of the following conditions are met:

- a) eTSEC1\_ATTR[RDSSEN and/or RBDSEN] is set
- b) System is configured to Wake-on-LAN

Impact:

In a very rare condition, the system may hang when the erratum is encountered

Workaround:

Use one of the following two workarounds:

Workaround 1:

a) Clear eTSEC1\_ATTR[ELCWT, BDLWT, RDSSEN and RBDSEN] just before setting GUTs\_POWMGTCR[DPSTP] bit.

b) Set eTSEC1\_ATTR[ELCWT, BDLWT, RDSSEN or RBDSEN] bit(s) as desired in wake-up routine before CPU(s) process any Ethernet frame or eTSEC buffer descriptors.

Workaround 2:

Configure the system to have eTSEC BDs and frame data reside in non-cacheable region

Severity or Errata:

Minor, low priority, no plan to fix

Device Affected:

P1022/P1013

REASON FOR CHANGE

There is a new errata addition to the Chip errata of P1022 device with low impact and rare occurrence.

**ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM FIT FUNCTION OR RELIABILITY )**

There is no change to form, fit, function or reliability.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to [www.freescale.com](http://www.freescale.com)

**UAL DATA AVAILABILITY DATE:** 16-Dec-2014

**UALIFICATION STATUS:** N/A

**UALIFICATION PLAN:**

N/A

**RELIABILITY DATA SUMMARY :**

N/A

**ELECTRICAL CHARACTERISTIC SUMMARY :**

N/A

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**CHANGED PART IDENTIFICATION:**

N/A

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**ATTACHMENT(S):**

External attachment(s) FOR this notification can be viewed AT:

[16581\\_Deep\\_Sleep\\_erratum\\_PB16581.pdf](#)

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