



Erratum A-007836: The system may hang upon enabling L2 cache, after coming out of deep sleep

PB #16581

Affected Devices: P1022/P1013

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Erratum L2C A-007836: The system may hang upon enabling L2 cache, after coming out of deep sleep

Description:

The system may hang when L2 is re-enabled after waking up from deep sleep if all of the following conditions are met:

- eTSEC1_ATTR[RDSEN and/or RBDSEN] is set
- System is configured to Wake-on-LAN

Impact:

In a very rare condition, the system may hang when the erratum is encountered

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Workaround:

Use one of the following two workarounds:

Workaround 1:

- Clear eTSEC1_ATTR[ELCWT, BDLWT, RDSSEN and RBDSEN] just before setting GUTs_POWMGTCR[DPSTP] bit.
- Set eTSEC1_ATTR[ELCWT, BDLWT, RDSSEN or RBDSEN] bit(s) as desired in wake-up routine before CPU(s) process any Ethernet frame or eTSEC buffer descriptors.

Workaround 2:

- Configure the system to have eTSEC BDs and frame data reside in non-cacheable region

Severity or Errata: Minor, low priority, no plan to fix

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