
PRODUCT BULLETIN

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ISSUE DATE: 01-Jul-2015
NOTIFICATION: 16820
TITLE: i.MX 6Dual/6Quad Errata Revision Update
EFFECTIVE DATE: 02-Jul-2015

DEVICE(S)

MPN
MCIMX6D4AVT08A
MCIMX6D4AVT08AB
MCIMX6D4AVT08AC
MCIMX6D4AVT08ACR
MCIMX6D4AVT08AD
MCIMX6D4AVT08ADR
MCIMX6D4AVT10A
MCIMX6D4AVT10AB
MCIMX6D4AVT10AC
MCIMX6D4AVT10ACR
MCIMX6D4AVT10AD
MCIMX6D4AVT10ADR
MCIMX6D5EYM10A
MCIMX6D5EYM10AC
MCIMX6D5EYM10ACR
MCIMX6D5EYM10AD
MCIMX6D5EYM10ADR
MCIMX6D5EYM10C
MCIMX6D5EYM10CC
MCIMX6D5EYM12A
MCIMX6D5EYM12AC
MCIMX6D5EYM12AD
MCIMX6D5EYM12C
MCIMX6D5EYM12CC
MCIMX6D5EZK08AD
MCIMX6D5EZK10AC
MCIMX6D5EZK10AD

MCIMX6D6AVT08A
MCIMX6D6AVT08AB
MCIMX6D6AVT08AC
MCIMX6D6AVT08ACR
MCIMX6D6AVT08AD
MCIMX6D6AVT08ADR
MCIMX6D6AVT10A
MCIMX6D6AVT10AB
MCIMX6D6AVT10AC
MCIMX6D6AVT10ACR
MCIMX6D6AVT10AD
MCIMX6D6AVT10ADR
MCIMX6D7CVT08A
MCIMX6D7CVT08AC
MCIMX6D7CVT08AD
MCIMX6D7CZK08AD
MCIMX6DP4AVT1AA
MCIMX6DP4AVT8AA
MCIMX6DP5EYM1AA
MCIMX6DP6AVT1AA
MCIMX6DP6AVT8AA
MCIMX6DP7CVT8AA
MCIMX6Q4AVT08A
MCIMX6Q4AVT08AB
MCIMX6Q4AVT08AC
MCIMX6Q4AVT08ACR
MCIMX6Q4AVT08AD
MCIMX6Q4AVT08ADR
MCIMX6Q4AVT10A
MCIMX6Q4AVT10AB
MCIMX6Q4AVT10AC
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MCIMX6Q5EYM12AD
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MCIMX6Q6AVT10AC
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MCIMX6QP5EYM1AA
MCIMX6QP6AVT1AA
MCIMX6QP6AVT8AA
MCIMX6QP7CVT8AA
PCIMX6D4AVT08AA
PCIMX6D5DZK10AA
PCIMX6D5EVT10AA
PCIMX6D5EVT12AA
PCIMX6D5EYM10AC

PCIMX6D5EYM10AD
PCIMX6D5EYM10CC
PCIMX6D5EYM12AC
PCIMX6D5EYM12CC
PCIMX6D6AVT10AA
PCIMX6D6AVT10AB
PCIMX6D6AVT10AC
PCIMX6D6AVT10AD
PCIMX6D7CVT08AB
PCIMX6D7CVT10AA
PCIMX6DP5EYM1AA
PCIMX6DP6AVT1AA
PCIMX6DP7CVT8AA
PCIMX6Q0AVT01A
PCIMX6Q0AYM01A
PCIMX6Q4AVT10AA
PCIMX6Q5DVT12AA
PCIMX6Q5DZK10AA
PCIMX6Q5DZK10AB
PCIMX6Q5EVT10AA
PCIMX6Q5EYM10AA
PCIMX6Q5EYM10AB
PCIMX6Q5EYM10AC
PCIMX6Q5EYM10AD
PCIMX6Q5EYM10CC
PCIMX6Q5EYM12AC
PCIMX6Q5EYM12CC
PCIMX6Q5EZK08AD
PCIMX6Q5EZK10AC
PCIMX6Q5EZK10AD
PCIMX6Q6AVT10AA
PCIMX6Q6AVT10AB
PCIMX6Q6AVT10AC
PCIMX6Q6AVT10AD
PCIMX6Q6AVT10AX
PCIMX6Q6AYM10AA
PCIMX6Q7CVT08AB
PCIMX6Q7CVT10AA

PCIMX6Q7CZK08AD
PCIMX6QP5EYM1AA
PCIMX6QP6AVT1AA
PCIMX6QP7CVT8AA
PCIMX6S5DVV10AA
PCIMX6S6AVV08A
PCIMX6U5DZT10AA
PCM2A114BBC
SC6Q888YM10AC
SCIMX6Q7CZK08AD

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AFFECTED CHANGE CATEGORIES

- ERRATA

DESCRIPTION OF CHANGE

The i.MX6Dual and i.MX6Quad errata has been updated with a new revision.

ERR006282: ROM code uses nonreset PFDs to generate clocks, which may lead to random boot failures.

Further clarification on this errata had been updated as well as the **Proposed Solution** to indicate ERR006282 is fixed in silicon revision 1.3.

The updated Rev5 errata can be found on Freescale's website here:

http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=i.MX6Q&fpc=1&tab=Documentation_Tab

REASON FOR CHANGE

The updated errata has been updated on the i.MX6Dual/Quad documentation site.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No impacts have been made to device for form, fit, function or reliability.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 19-Jun-2015

QUALIFICATION STATUS: N/A

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[16820_IMX6DQCE.pdf](#)
