
PRODUCT BULLETIN

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ISSUE DATE: 03-Sep-2015
NOTIFICATION: 16849
TITLE: eSDHC Erratum A-009204; 837x, 8536, 8569, 830x, P1020, P1022, P2020, and P4080 Family of Devices
EFFECTIVE DATE: 04-Sep-2015

DEVICE(S)

MPN
AOP-P1015NSE5DFB
AOP-P2020XSE2KFB
DUMMYMATERIAL
EQN-P2010EVTAN
EQN-P2010EVTAQ
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SCWP2020C

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AFFECTED CHANGE CATEGORIES

- ERRATA

DESCRIPTION OF CHANGE

Erratum number **A-009204** is being released that impacts the eSDHC block.

Title: System bus may hang if register SYSCTL[RSTD] is set while DMA transactions are active

Description: In the event of that any data error (like, IRQSTAT[DCE]) occurs during an eSDHC data transaction where DMA is used for data transfer to/from the system memory, setting the SYSCTL[RSTD] register may cause a system hang. If software sets the register SYSCTL[RSTD] to 1 for error recovery while DMA transferring is not complete, eSDHC may hang the system bus. This happens because the software register SYSCTL[RSTD] resets the DMA engine without waiting for the completion of pending system transactions

Impact: Causes system bus hang

Workaround: Add a 5 ms delay before setting SYSCTL[RSTD] to make sure all the DMA transfer are finished.

REASON FOR CHANGE

Alert customers of new eSDHC A-009204 erratum release and the workaround

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No impact of product change to form, fit, function or reliability

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUALIFICATION STATUS: N/A

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[16849 Erratum A-009204.pdf](#)
